

Docket No.: GR 00 P 1582

CERTIFICATION

I, the below named translator, hereby declare that: my name and post office address are as stated below; that I am knowledgeable in the English and German languages, and that I believe that the attached text is a true and complete translation of the German language text of Application No. 09/817,967 filed March 27, 2001.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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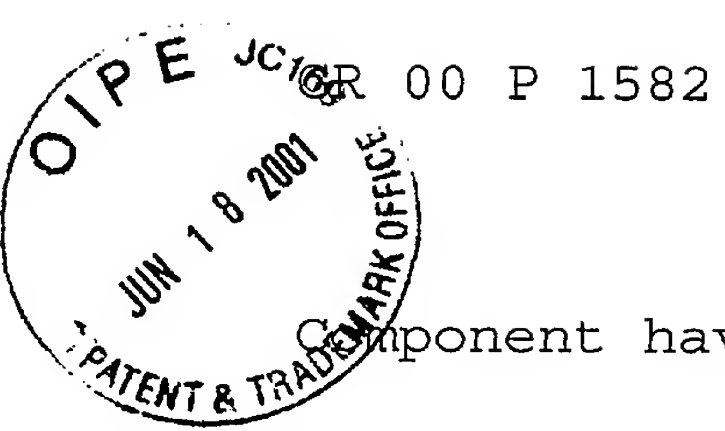
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09/817,967



Component having at least two adjacent insulating layers and
production process therefore

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The invention relates to a component having at least two
adjacent insulating layers and to a production process
therefore.

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In electronic and microelectronic components, neighboring
electrical interconnects are isolated from one another by an
insulator (insulating layer). In order to insulate the
interconnects next to one another or above one another,
especially in an integrated circuit (IC) or a multichip module
(MCM), a high-temperature-stable organic or inorganic material
is used. In this case, materials such as e.g. polyimide,
benzocyclobutene, polybenzoxazole and/or silicon dioxide are
used. The choice of the material is dictated by the physical
property of the insulator (low-stress behaviour;

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photosensitivity; water-vapor permeability; oxygen
permeability; metal diffusibility etc.) which is regarded as
important, because the materials which can be used have
different physical property profiles.

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The physical weaknesses of the chosen insulating material are
either not compensated for or are compensated for by an extra

covering (e.g. to reduce the water-vapor permeability), a substrate back coating (e.g. to lessen stress-induced substrate flexion) or another auxiliary structure.

5 It is an object of the present invention to provide a component which includes at least two insulating layers, with the possibility of compensating for the physical weaknesses of one insulating layer by the subsequent insulating layer. A further subject of the invention is a process for the
10 production of such a component.

15 The object of the invention is achieved by and the subject of the invention is a component which includes a substrate and at least one upper and one lower insulating layer, which are adjacent to each other and whose layer thickness is in the range of between 0.05 and 50 μm , at least one region of one insulating layer being activated for subsequent metallization, for photosensitization, for hydrophobicization and/or for
20 other surface functionalization.

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A further subject of the invention is a process for the production of a component, a lower insulating layer being applied to a substrate and optionally patterned in a first working step, at least one region of the lower insulating
25 layer being activated in a second working step and at least one second, upper insulating layer being applied to the lower,

activated insulating layer and patterned in a third working step.

A final subject of the invention is a process for the production of a component, an insulating layer being applied to a substrate and optionally patterned in a first working step, a further insulating layer being applied and patterned in a second working step, and one of the two insulating layers being activated in a third working step.

The component is preferably an electronic or microelectronic component. It has, again preferably, two insulation layers which are chemically different and, accordingly, also have different physical property profiles. For instance, the first layer may exhibit good low-stress behaviour but have excessive water permeability, and the second layer may be water-impermeable so as to compensate for the permeability of the first insulation layer. One layer is activated and is intended to provide the electrically conductive layer later on, for example, in the wafer production process by seeding with a seed solution and subsequent metallization. In spite of the metallization or other subsequent processing, the initially activated insulation layer keeps its desired physical properties, e.g. water impermeability.

The number of layers located above one another is by no means restricted to two, but may be as many as desired, depending on the component, and it is also possible for two similar layers to be located above one another if the lower layer is
5 activated before the upper layer is applied.

It has been found to be advantageous if the upper insulating layer is used as a mask during the activation of the lower insulating layer. To that end, the patterning of the upper
10 insulating layer must take place in the process prior to the activation. By exposure, developing, drying and/or baking, it is possible to pattern the upper, preferably photosensitive insulating layer. If the upper insulating layer is not
15 photosensitive, then the mask can be produced by conventional coating with photoresist and patterning the latter, then etching back the upper insulating layer.

According to another embodiment, the lower insulating layer is activated before the upper insulating layer is applied.
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Which of the two layers is activated, is dictated primarily by whether the activator in question reacts via its constituents only with one layer and, if the activator could potentially activate both layers, when the activator is employed, i.e.
25 which surface it encounters.

The term "insulating layer or insulation layer" is here intended to mean an electrically insulating material which remains in the component after this component has been fabricated, i.e. is not removed. This is not intended to

5 include materials which are used as patterning auxiliaries and are removed after a process (e.g. metallization, etching) has been carried out, for instance commercial photoresists based on novolak. Likewise, materials which are a constituent of the substrate (e.g. printed circuit boards based on epoxy resin) or are used as a covering (e.g. a passivation layer on a silicon oxide and/or nitride IC or an IC package of filled epoxy resin, i.e. a mould compound) are not "insulating layers" in the sense of the term used here.

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15 The thickness of the insulating layer is preferably between 0.05 and 50 μm , particularly preferably between 0.1 and 20 μm .

The insulating layer preferably consists of a polymer. The polymer advantageously has high chemical and thermal
20 stability. This allows it to withstand soldering and cleaning processes as well as the activation (chemical and/or physical) without being damaged. In particular, the use of the following types of polymers has proved to be advantageous: dielectrics such as polyimides (such as e.g. in US 3,957,512 and
25 EP 0 027 506 B1), polybenzoxazoles (such as e.g. disclosed by EP 0 023 662 B1 and EP 0 264 678 B1), polybenzimidoxazoles;

predominantly aromatic polyethers, polyether ketones,
polyether sulphones; benzocyclobutene, aromatic hydrocarbons,
polyquinolines, polyquinoxalines, polysiloxanes (silicones),
polyurethanes or epoxy resins. Copolymers or mixtures of these
5 polymers with one another are likewise suitable. Also suitable
are compounds or polymers with organic-inorganic structure,
such as e.g. organosilicon, organophosphorus or organoboron
compounds. As is known, all the material classes may either be
applied in their final form (spin coating, screen printing
10 etc.) or, alternatively, they are vapor-deposited on the
substrate, or on the first insulation layer, in a preliminary
stage and the polymer is produced there. Examples of layers
which can be produced on the substrate, or on an insulating
layer, include layers of carbon, a-C:H (amorphous) as well as
15 a-C:H layers with further elements such as Si, P, O or B.
Purely inorganic materials such as silicon oxide and nitride
are only included in this category if, as the upper of the two
layers, they are applied and patterned or are applied using a
shadow mask, a printing technique and/or lithography.

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In principle, all materials are suitable which are stable
during the processes to be carried out, exhibit good
electrical insulation and do not have a perturbing effect on
the finished component. Photosensitive formulations of the
25 insulating materials are especially suitable.

The insulating layer may also contain several of the aforesaid constituents and a filler. Especially for use as a paste, but also for screen printing, a suitable filler may be added to the insulating material. It may, for example, be applied to the substrate in dissolved form or as a paste. Examples of suitable techniques include spin coating, casting, dispensing, scraping, tampon printing, inkjet printing and/or screen printing.

A first insulating layer is applied to the substrate, for example by spin coating technique, and is cured if appropriate, when this is necessary in order to obtain the final properties. The second insulation layer is then applied to the first, and is dried if appropriate. Next, e.g. for patterning, it is exposed through a mask, developed, dried and optionally cured. The subsequent treatment of the resulting layer sandwich with an activator leads to selective activation of either the upper or the lower insulation layer. It is also possible to apply the second insulating layer ready-patterned, e.g. by printing.

The term "curing" refers to any process by which soluble products are converted into insoluble products. Examples include baking, cyclicizing and cross-linking.

The surface activation of the insulating layer in question can, as explained in more detail below, be carried out using a physical process and/or using a chemical process.

- 5 The activation can be carried out by immersion, etching, exposure, irradiation, sputtering, heating, partial dissolving, wetting or another known technique.

10 Depending on the embodiment, the activator is a gas (or a gas mixture), a liquid, a solution or a plasma. The activator may, in particular, also be a combination of a gas with a liquid or a different combination of several activators. The activation selectively modifies an insulation layer or the surface of an insulation layer, for example so that subsequently only this layer can be seeded and/or metallized. Other types of activation, e.g. hydrophobicization, photosensitization and/or other types of surface functionalization, also fall within the scope of the invention.

- 20 Liquid activators are e.g. basic reagents such as solutions of one or more alkali metal and/or alkaline earth metal hydroxides, ammonium hydroxides; oxidizing reagents such as hydrogen peroxide, chromate, permanganate, (per)chlorate and/or peroxosulphate solution; solutions which contain an acid such as sulphuric, hydrochloric, nitric and/or phosphoric
- 25

acid. All the solutions may be used individually or in any desired combination.

Examples of activators which are in plasma form include:

5 oxygen, chlorine, carbon dioxide, sulfur dioxide, noble gas and/or ammonia plasmas; examples of suitable gases include ozone, oxygen, halogens and/or sulfur dioxide, and mixtures thereof.

10 The seed solution is a solution or emulsion of a metal (or of a metal compound) in ionogenic or colloidal form. This solution may be neutral, basic or acidic. Preferred seed solutions are all solutions of metals and non-metals, or compounds thereof, which catalyze the subsequent deposition of
15 a metal (such as e.g. copper or nickel). The seed solution preferably contains noble metals (ruthenium (Ru), rhodium (Rh), palladium (Pd), osmium (Os), iridium (Ir), platinum (Pt), silver (Ag), gold (Au)), or compounds and complexes thereof (organic and/or inorganic).

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The substrate is preferably a semiconductor (silicon (Si), gallium arsenide, germanium (Ge)) or ceramic, and it may already have electronic circuits including metal and insulating layers below the first insulating layer (e.g. a
25 front-end processed substrate). The substrate may, however, also be glass, a printed circuit board and/or a metal. The

substrate may furthermore be one of the aforesaid materials with an applied insulating layer.

The process is advantageous, in particular, because many electronic and/or microelectronic components - before they are processed with the at least two insulating layers - are coated with a buffer coating (for example, wafers in the front-end field come in this form; the buffer coating is, for example, polyimide or polybenzoxazole; the inorganic passivation layers of silicon nitride and/or oxide, for example, are located underneath). This buffer coating may already be an insulating layer in the sense of the invention, i.e. after the second layer has been applied and patterned, it may be activated and, according to one embodiment of the process, it may be coated with a second insulating layer or activated.

The invention will be explained in more detail with reference to exemplary embodiments.

Example 1

A commercially available wafer with an already cured, i.e. cyclicized and/or baked, polyimide coating is activated as follows: the polyimide is activated by a short etching operation lasting 25 s in an oxygen plasma (500 W, 50 sccm oxygen, 50 mTorr). The activated wafer is immersed for 10 s in

demonized water, the water is spun off and the wafer is dried for 60 s at 120°C. A second coat of a photosensitive polyimide is then spin-coated on, patterned and baked in an oven at 400°C under nitrogen. The plasma activation creates the activated surface, to which metal seeds (e.g. a metal complex) can bind selectively.

Example 2

10 A photosensitive polyimide is, according to the manufacturer's specifications, spin-coated onto a silicon substrate, exposed, developed and baked (see Example 1). To activate the polyimide, the substrate is then immersed in the following solutions:

- 15 - 10 min in an alkaline permanganate solution, consisting of 140 g/l sodium permanganate and 50 g/l sodium hydroxide, at a temperature of 40°C
- 20 - washing in deionized water
- immersion for 3 min at room temperature in semi-concentrated sulfuric acid (5 mol/l)
- 25 - washing in deionized water.

A second coat of polyimide is then spin-coated on and, as described above, patterned and cured (see Example 1).

Example 3

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The dielectric polybenzoxazole is applied to a silicon wafer by the spin-coating technique, pre-dried at 100°C and cured on a hotplate for 1 min each at 200°C, 260°C and 350°C under nitrogen. The surface is then activated in a water-gas plasma (CO:H₂ as 1:1; 500 W, 50 sccm, 50 mTorr). The activated surface is immersed for 10 s in deionized water, the water is spun off and the wafer is dried for 60 s at 120°C. A second coat of polybenzoxazole is then applied by screen printing, pre-dried and - as above - cured. The plasma activation creates a surface containing carboxyl groups.

Example 4

A photosensitive polyimide is applied to a silicon substrate by a horizontal spin-coater at 5000 rpm. The spin-coating time is 20 s. The film is subsequently pre-dried for 3 min at 100°C on a hotplate and then heat-treated (cured) in an oven under nitrogen flushing for 30 min at 400°C. After cooling to room temperature, a photosensitive polybenzoxazole (PBO) is spin-coated onto the polyimide layer; dried on a hotplate, exposed through a mask using an illumination device and developed in

an aqueous alkaline solution, washed and dried. The substrate is heat-treated on a hotplate with the following profile: 10°C/min to 150°C, 5°C/min to 280°C, holding time 10 min. It is then cooled to room temperature. The PBO is used as a mask for the polyimide. To activate the polyimide, the substrate is subsequently immersed for 10 min in an alkaline permanganate solution, consisting of 140 g/l sodium permanganate and 50 g/l sodium hydroxide, at a temperature of 40°C, washed with deionized water and then immersed in 5 mol/l sulfuric acid for 3 min. It is subsequently washed with deionized water.

The polybenzoxazole layer also serves to reduce significantly the relatively high water-vapor permeability of polyimide.

Example 5

Similar to Example 4, but the polyimide is activated using a short plasma-etching operation with oxygen in a reactive ion etcher (30 sccm oxygen, 500 W, 70 mTorr 10s) with subsequent conditioning for 3 min in a conditioning bath. The latter may, for example, be a 0.5 molar sodium hydroxide solution in water.

Example 6

A photosensitive PBO is spin-coated onto a substrate with a silicon nitride surface and dried on a hotplate, and subsequently heat-treated (cured) on a high-temperature hotplate under nitrogen at 350°C. After cooling to room temperature, a polyimide is spin-coated onto the PBO layer and dried on a hotplate. Using an illumination device, the polyimide is patterned, exposed and developed, washed (isopropanol, isopropanol/deionized water (1:1) and finally deionized water), then dried. To cure the polyimide, the substrate is heat-treated in an oven under nitrogen flushing for 60 min at 350°C. After cooling to room temperature, to activate the polyimide, the substrate is immersed for 10 min in a 1.5% strength by weight sodium hydroxide solution at a temperature of 40°C, washed with deionized water and then immersed in 5 mol/l sulfuric acid for 3 min.

The polybenzoxazole layer between the substrate and the polyimide is advantageous since it serves as a stress-compensation layer and adheres better at the two interfaces than the polyimide adheres to the substrate.

Example 7

PBO is spin-coated onto a substrate as in Example 6 and is heat-treated. Using the stencil printing process, a cyclothene layer (benzocyclobutene, BCB) is applied to the PBO layer and

baked for 30 min at 250°C. The BCB is activated by immersing the substrate in a 1.5% strength sodium hydroxide solution at 40°C for 5 min.

5 Example 8

A photosensitive polyimide is spin-coated onto a substrate, then dried for 2 min at 110°C and subsequently heat-treated at 350°C for 90 min. As a mask for the selective activation of the polyimide, and to reduce its gas and vapor permeability, a 0.5 μm thick amorphous hydrocarbon layer is deposited using a shadow mask by means of a CVD (chemical vapor deposition) process. The polyimide is subsequently activated as in Example 4. The metallization can likewise be carried out according to Example 4.

Example 9

A polyimide is spin-coated onto a silicon substrate (20 s at 5000 rpm), subsequently dried (3 min at 100°C on a hotplate) and heat-treated for 30 min on a hotplate at 350°C. After cooling to room temperature, another, photosensitive polyimide is spin-coated on, dried at 90°C, exposed, developed, washed (isopropanol, isopropanol/deionized water (1:1) and finally deionized water) subsequently dried and heat-treated at 400°C.

The non-photosensitive polyimide, which forms the lower layer, is activated as follows:

Immersing (10 min) in a 1.5% strength sodium hydroxide solution at a temperature of 45°C, washing with deionized water and subsequent immersing in a 1M HCl solution at 30°C for 30 min, washing again with deionized water.

Example 10

A wafer with a 4 μ m thick pre-cyclicized polyimide layer ("substrate") is processed as follows: a water-impermeable silicon nitride layer (50 nm) is applied to the substrate by a CVD process and the nitride layer is patterned using a photoresist (exposing and developing the photoresist, dry-chemically etching the nitride layer with CHF₃/O₂, stripping the photoresist). The plasma etching is stopped at the underlying polyimide coat. The plasma etching creates an activated surface.

Example 11

Similar substrate to Example 10, on which the silicon oxide layer is applied pre-structured via a shadow mask. The bare polyimide is activated by immersion (1 min) in concentrated nitric acid at 50°C.

According to the invention, it is for the first time possible,
in the case of a component such as a wafer with a
conductor/insulator/conductor layer structure, to combine the
5 physical properties of two insulating layers, one of the
insulating layers being selectively activated and capable of
being rendered electrically conductive by a metallization or
the like.

10 Examples of photosensitive polyimides and polybenzoxazoles:

polyimide: EP 0 027 506 B1

polybenzoxazole: EP 0 023 662 B1, EP 0 264 678 B1